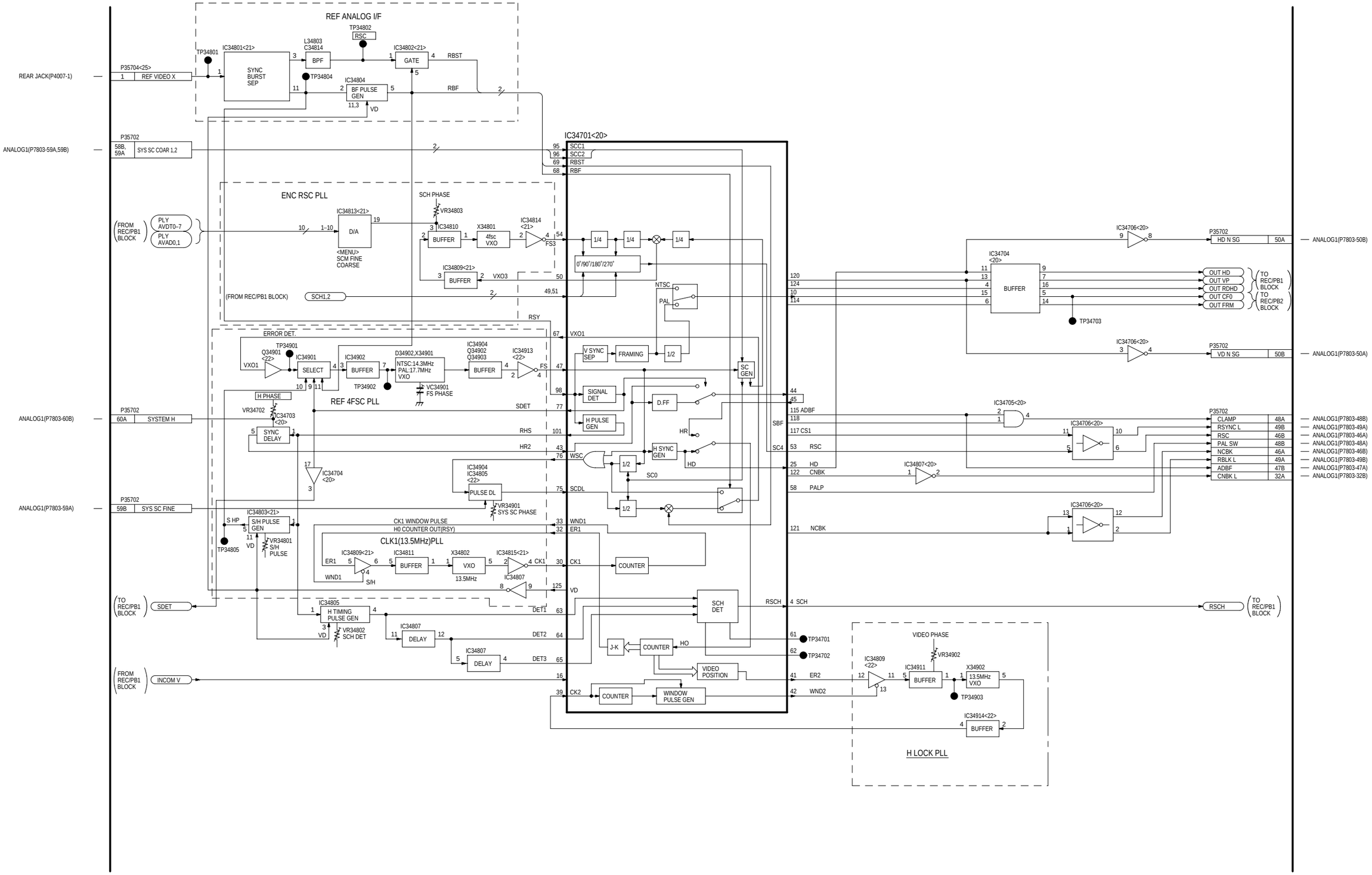
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TP34802
RSC
TP34801
IC34801<21>
L34803
C34814
IC34802<21>
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BPF
GATE
RBST
REAR JACK(P4007-1)
P35704<25>
1
REF VIDEO X
TP34804
IC34804
BF PULSE GEN
RBF
11,3
VD
P35702
ANALOG1(P7803-59A,59B)
58B, 59A
SYS SC COAR 1,2
IC34701<20>
SCC1
SCC2
RBST
RBF
ENC RSC PLL
SCH PHASE
VR34803
IC34813<21>
D/A
IC34810
BUFFER
X34801
4fsc VXO
IC34814 <21>
FS3
<MENU> SCM FINE COARSE
IC34809<21>
VXO3
(FROM REC/PB1 BLOCK)
PLY AVDT0–7
PLY AVAD0,1
SCH1,2
RSY
ERROR DET.
TP34901
Q34901 <22>
IC34901
VXO1
SELECT
IC34902
D34902,X34901
NTSC:14.3MHz
PAL:17.7MHz
VXO
TP34902
VC34901
FS PHASE
IC34904
Q34902
Q34903
IC34913 <22>
FS
REF 4FSC PLL
H PHASE
VR34702
IC34703 <20>
SYNC DELAY
SDET
RHS
HR2
VXO1
V SYNC SEP
FRAMING
1/2
SC GEN
SIGNAL DET
D.FF
H PULSE GEN
H SYNC GEN
HR
HD
WSC
SC0
IC34704 <20>
IC34904
IC34805 <22>
PULSE DL
VR34901
SYS SC PHASE
SCDL
ANALOG1(P7803-60B)
P35702
60A
SYSTEM H
ANALOG1(P7803-59A)
P35702
59B
SYS SC FINE
IC34803<21>
S/H PULSE GEN
S HP
TP34805
VR34801
S/H PULSE
CK1 WINDOW PULSE
H0 COUNTER OUT(RSY)
WND1
ER1
CLK1(13.5MHz)PLL
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IC34811
BUFFER
X34802
VXO
13.5MHz
IC34815<21>
CK1
S/H
IC34807
VD
COUNTER
NTSC
PAL
0°/90°/180°/270°
SBF
SC4
RSCH
4 SCH
SCH DET
TO REC/PB1 BLOCK
SDET
IC34805
H TIMING PULSE GEN
VR34802
SCH DET
DET1
DET2
DET3
DELAY
J-K
HO
VIDEO POSITION
WINDOW PULSE GEN
CK2
FROM REC/PB1 BLOCK
INCOM V
TP34701
TP34702
ER2
WND2
VIDEO PHASE
VR34902
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IC34911
X34902
13.5MHz VXO
TP34903
IC34914<22>
H LOCK PLL
IC34706<20>
IC34704 <20>
OUT HD
OUT VP
OUT RDHD
OUT CF0
OUT FRM
TO REC/PB1 BLOCK
TO REC/PB2 BLOCK
TP34703
VD N SG
50B
ANALOG1(P7803-50A)
HD N SG
50A
ANALOG1(P7803-50B)
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IC34807<20>
CLAMP
RSYNC L
RSC
PAL SW
NCBK
RBLK L
ADBF
CNBK L
48A
49B
46A
48A
46B
49A
47B
32A
ANALOG1(P7803-48B)
ANALOG1(P7803-49A)
ANALOG1(P7803-46A)
ANALOG1(P7803-48A)
ANALOG1(P7803-46B)
ANALOG1(P7803-49B)
ANALOG1(P7803-47A)
ANALOG1(P7803-32B)